

STUDY MODULE DESCRIPTION FORM		
Name of the module/subject Selected issues of the theory of circuits		Code 1010325311010324872
Field of study Electrical Engineering	Profile of study (general academic, practical) (brak)	Year /Semester 1 / 1
Elective path/specialty -	Subject offered in: Polish	Course (compulsory, elective) obligatory
Cycle of study: Second-cycle studies	Form of study (full-time, part-time) part-time	
No. of hours Lecture: 20 Classes: 10 Laboratory: - Project/seminars: -		No. of credits 3
Status of the course in the study program (Basic, major, other) (brak)		(university-wide, from another field) (brak)
Education areas and fields of science and art technical sciences Technical sciences		ECTS distribution (number and %) 3 100% 3 100%
Responsible for subject / lecturer: Dr inż. Jarosław Jajczyk email: jaroslaw.jajczyk@put.poznan.pl tel. 616652659 Elektryczny ul. Piotrowo 3A, 60-965 Poznań		
Prerequisites in terms of knowledge, skills and social competencies:		
1	Knowledge	Basic information form math and physics at level of first degree studies.
2	Skills	Skills in enhanced understanding and interpretation of information and effective self-education in range of Science related with chosen field of study.
3	Social competencies	Student should have enhanced consciousness of necessity of improving his competences, readiness to work individual and cooperate within groups.
Assumptions and objectives of the course: Presentation of: passive and active filters, nonlinear elements of circuits, nonlinear electric and magnetic circuits, ferroresonance, signals and theory of signals flow, signals flow graphs, structure matrix. Presentation of enhanced analytic methods of solving electric circuits.		
Study outcomes and reference to the educational results for a field of study		
Knowledge: 1. describe electric filters, nonlinear circuits and signals, describe and explain laws and methods of: analysis of filters, nonlinear electric circuits and electric signals - [K_W02++, K_W04+, K_W06+++, K_W09++] 2. recognize and select proper methods of enhanced electric circuits analysis - [K_W02++, K_W04+]		
Skills: 1. use knowledge in field of enhanced theory of electric and electronic circuits, necessary to determine parameters of circuits, such as : attenuation, characteristic impedance, static and dynamic resistance, signals transfer function etc. - [K_U01++, K_U02++, K_U07+] 2. get specialized information from literature and web, work individual and in workgroups, solve exercises in field of enhanced theory of circuits by his own and within workgroup - [K_U01++, K_U2++, K_U07+]		
Social competencies: 1. think and operate in enterprising way in the field of enhanced analysis of basic electric circuits - [K_K01++, K_K02+]		
Assessment methods of study outcomes		

<p>Lecture: - assess the knowledge and skills listed on the written and oral exam of enhanced knowledge of electrical engineering.</p> <p>Ćwiczeni audytoryjne: - assess skills of solving advance accounting exercises in field of analysis of electric circuits ? verification skills on every exercises and two tests during the semester.</p> <p>Obtaining additional points activity during exercises, in particular way for: - proposing to discuss additional aspects of the subject, - effective use of knowledge obtained during solving of given problem, - comments related to improve teaching material, - aesthetics of solved problems and reports ? within self education.</p>		
Course description		
<p>Signals block diagrams: creation, conversion and simplification of block diagrams, transfer functon determination, signal flow graphs, rules of reduction of signal flow graphs, cascade connection, inversion of branches, passive and active filter, ? and ? and X type filters, band-pass filters, nonlinear, direct and alternating current, circuits, techniques of analysis, branched and unbranched magnetic circuits, solving magnetic circuits, nonlinear circuits with ferromagnetic elements, phenomenon of ferroresonance, oscillations in non linear circuits.</p>		
<p>Basic bibliography: 1. Kurdziel R.: Podstawy elektrotechniki, WNT, Warszawa 1973. 2. Bolkowski S.: Teoria obwodów elektrycznych, WNT, Warszawa 1998. 3. Szabatin J., Śliwa E.: Zbiór zadań z teorii obwodów. Część 1, Wydawnictwo Politechniki Warszawskiej, Warszawa 1997. 4. Mikołajuk K., Trzaska Z.: Zbiór zadań z elektrotechniki teoretycznej, WNT, Warszawa 1978.</p>		
<p>Additional bibliography: 1. Krakowski M.: Elektrotechnika teoretyczna, PWN, Warszawa 1978. 2. Chua L.O.,Desoer C.A.,Kuh E.S.: Linear and Nonlinear Circuits, McGraw-Hill Inc., 1987. 3. Jastrzębska G.,Nawrowski R.: Zbiór zadań z podstaw elektrotechniki, Wydawnictwo Politechniki Poznańskiej, Poznań 2000.</p>		
Result of average student's workload		
Activity	Time (working hours)	
1. Participation in the lectures	20	
2. Participation in the auditorium exercises	10	
3. Participation in consultations on the lecture	2	
4. Participation in consultations on the auditorium exercises	8	
5. Preparation for the auditorium exercises	8	
6. Homeworks	8	
7. Preparation for the exam	20	
8. Preparation for the auditorium exercises pass	10	
9. Participation in the exam	3	
10. Participation in the colloquium	3	
Student's workload		
Source of workload	hours	ECTS
Total workload	92	3
Contact hours	46	2
Practical activities	0	0